IN THE CLAIMS

1) (Cancelled)

2) (Presently Amended). The ~~method~~ <u>thumb drive</u> recited in claim ~~1~~ <u>21</u> wherein there are less banks of MRAM memory than there are sectors in said flash memory.

3) (Presently Amended). The ~~method~~ <u>thumb drive</u> recited in claim ~~1~~ <u>21</u> wherein said flash memory is NAND flash memory.

4) (Cancelled).

5) (Presently amended). The ~~method~~ <u>thumb drive</u> recited in claim ~~1~~ <u>21</u> wherein a MRAM data bank is disassociated from a sector in said flash memory when data from said MRAM bank is transmitted to the associated sector in said flash memory.

6) (Cancelled)

7) (Presently Amended). The ~~method~~ <u>thumb drive</u> in claim ~~1~~ <u>21</u> wherein each MRAM memory bank is at least as large as a sector in said flash memory.

8) (Cancelled)

9) (Cancelled) .

10) (Presently Amended). The ~~cache~~ thumb drive recited in claim 8 21 wherein said ~~including an~~ embedded processor ~~that~~ receives and decodes commands received from said USB bus.

11) (Cancelled).

12) (Presently Amended). The ~~cache~~ thumb drive recited in claim 8 21 wherein there are less banks of MRAM memory than there are sectors in said flash memory.

13) (Cancelled)

14) (Cancelled)

15) (Cancelled).

16) (Cancelled).

17) (Cancelled).

18) (Cancelled).

19) (Cancelled).

20) (Presently Amended). The ~~method~~ thumb drive recited in claim ~~15~~ 21 wherein said ~~cache~~ thumb drive does not lose any data if power to said thumb drive ~~cache~~ is lost.

21) (New). A thumb memory drive including,

an embedded processor that provides an interface between said thumb memory drive and a Universal Serial Bus (USB) that is connected to a host processor,

a flash memory having a plurality of sectors,

said USB being adapted to transmit data to said thumb drive faster than said data can be stored in said flash memory,

a plurality of banks of Magnetic Random Access Memory (MRAM), each bank of said MRAM being capable of storing data for one sector of said flash memory,

said embedded processor being adapted to exclusively associate one bank of said MRAM with one sector of said flash memory,

multiport control logic which is adapted to direct data received from said USB that is destined for a particular sector of said flash memory to the associated bank of said MRAM memory and being adapted to transfer data from a bank of said MRAM to the associated sector of said flash memory when said bank of MRAM is full,

whereby data can be transferred and stored in said thumb drive at a faster rate than data can be stored in said MRAM memory and data is not lost if said thumb drive is disconnected from said host processor before data is stored in said flash memory.